This problem concerns paging (no virtual memory).

Suppose we have a virtual address of 48 bits and a physical address of 32 bits in a byte addressable machine. Pages are 8K bytes. Clearly state/justify your assumption concerning the size of a page table entry in each case (use multiples of bytes).

a. [5pt] How big is a conventional (single) page table for the above machine?

b. [5pt] How big is an inverted page table would be?